TOSHIBA CMOS Linear Integrated Circuit Silicon Monolithic

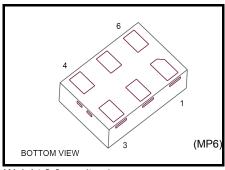
TC75S201L6X

Single Comparator

TC75S201L6X is a CMOS type general-purpose single comparator. The device can operate a single power supply and a lower supply current than a conventional bipolar general-purpose comparator. This devices's push-pull output stage can be directly connected to CMOS logic IC's.

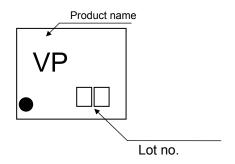
Features

- Full-Range Input/Output
- Small package
- Low input bias current
- Single power supply operation
- Low supply current: $IDD = 20\mu A$ (typ.)@ VDD=3.0V
- Push-pull output circuit

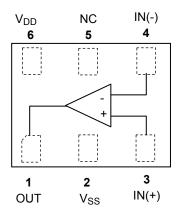


Weight: 2.0 mg (typ.)

Marking (Top View)



Pin Assignment (Top view)





Absolute Maximum Ratings

Characteristic	Symbol	Rating	Unit
Supply voltage	V_{DD}	±2.3 or 4.6	V
Differential input voltage ⁽¹⁾	ΔV_{IN}	±4.6	٧
Input voltage	V _{IN}	V _{SS} to V _{DD}	٧
Output current	lout	±35	mA
Power dissipation	P _D	250 (Note1)	mW
Operating temperature	T _{opr}	−40 to 85	°C
Storage temperature	T _{stg}	-55 to 125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note: Since this product sometimes brings about latch-up, which is peculiar to CMOS devices, note the following points:

- Don't raise the voltage level of the output pins beyond V_{DD}, nor lower it below V_{SS}.
 Consider the timing for power supply, too.
- Don't let any abnormal noise enter the device.

Note 1: Mounted on an FR4 board.

Operating Ranges (Ta = 25°C)

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Characteristic	Symbol	Rating	Unit	
Supply voltage	V _{DD} 1.5 to 4.		V	
Supply voltage	V_{DD}, V_{SS}	±0.75 to ±2.3	v	

Electrical Characteristics ($V_{DD} = 3.0V$, $V_{SS} = GND$, Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Input offset voltage	V _{IO}	_	_	±1	±6	mV	
Input offset current	I _{IO}	_	_	1	_	pA	
Input bias current	lį	_	_	1	_	pA	
Common-mode input voltage range	V _{ICM}	_	0	_	3.0	V	
Supply current	I _{DD} (Note)	_	_	20	44	μA	
Sink current	I _{SINK}	V _{OL} = 0.5V	6	18	_	mA	
Source current	ISOURCE	V _{OH} = 2.5V	3	15	_	mA	
High-level Output voltage	V _{OL}	I _{SINK} = 5.0 mA	_	0.15	0.35	V	
Low-level Output voltage	V _{OH}	I _{SOURCE} = 5.0 mA	2.65	2.85	_		
Propagation delay time (L/H)	t _{PLH}	Over drive = 100mV	_	600	_	ns	
Propagation delay time (H/L)	t _{PHL}	Over drive = 100mV	_	1000	_	ns	
Response time	tTLH	Over drive = 100mV	_	30	_	20	
	t _{THL}	Over drive = 100mV	_	24	_	ns	

(Note): Since this product causes an increase in current consumption with a rise in operational frequency, make sure that power consumption does not exceed the allowable dissipation.

Electrical Characteristics ($V_{DD} = 1.8V$, $V_{SS} = GND$, Ta = 25°C)

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Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Input offset voltage	V _{IO}	_	_	±1	±6	mV	
Input offset current	I _{IO}	_	_	1	_	pA	
Input bias current	lį	_	_	1	_	pA	
Common-mode input voltage range	V _{ICM}	_	0	_	1.8	V	
Supply current	I _{DD} (Note)	_	_	17	40	μA	
Sink current	I _{SINK}	V _{OL} = 0.5 V	3.0	8.0	_	mA	
Source current	ISOURCE	V _{OH} = 1.3 V	1.0	6.4	_	mA	
High-level Output voltage	V _{OL}	I _{SINK} = 3.0mA	_	0.15	0.35	\/	
Low-level Output voltage	V _{OH}	I _{SOURCE} = 3.0mA	1.45	1.65	_	V	
Propagation delay time (L/H)	t _{PLH}	Over drive = 100mV	_	590	_		
Propagation delay time (H/L)	t _{PHL}	Over drive = 100mV	_	1000	_	ns	
Response time	t _{TLH}	Over drive = 100mV	_	26	_	ns	
	t _{THL}	Over drive = 100mV	_	33	_		

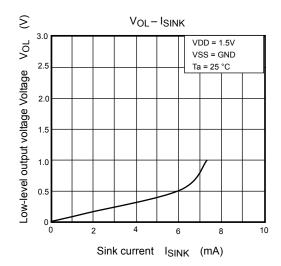
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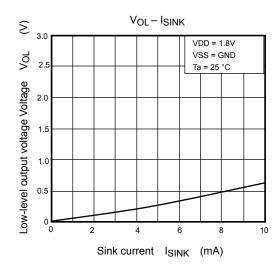
Electrical Characteristics ($V_{DD} = 1.5V$, $V_{SS} = GND$, Ta = 25°C)

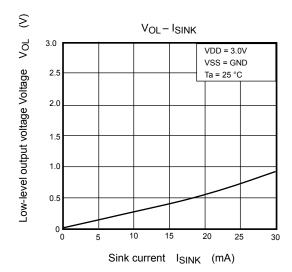
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Input offset voltage	V _{IO}	_	_	±1	±6	mV	
Input offset current	I _{IO}	_	_	1	_	pA	
Input bias current	lį	_	_	1	_	pA	
Common-mode input voltage range	V _{ICM}	_	0	_	1.5	V	
Supply current	I _{DD} (Note)	_	_	18	36	μA	
Sink current	I _{SINK}	V _{OL} = 0.5 V	1.0	5.9	_	mA	
Source current	Isource	V _{OH} = 1.0 V	0.5	4.3	_	mA	
High-level Output voltage	V _{OL}	I _{SINK} = 1.5mA	_	0.15	0.35	V	
Low-level Output voltage	V _{OH}	I _{SOURCE} = 1.5mA	1.15	1.35	_	V	
Propagation delay time (L/H)	t _{PLH}	Over drive = 100mV	_	600	_	ns	
Propagation delay time (H/L)	t _{PHL}	Over drive = 100mV	_	1000	_		
Response time	t _{TLH}	Over drive = 100mV	_	30	1	ns	
	t _{THL}	Over drive = 100mV	_	42	_	115	

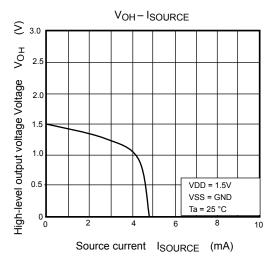
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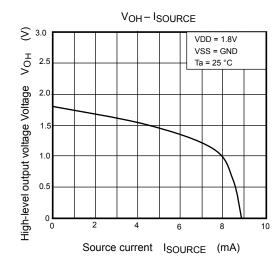
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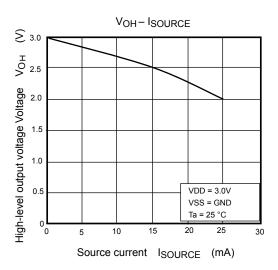








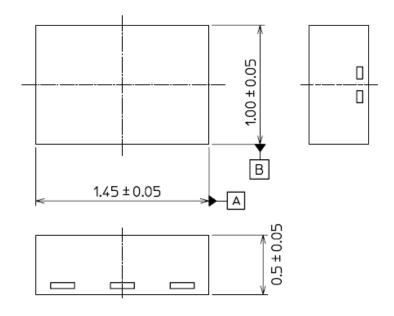


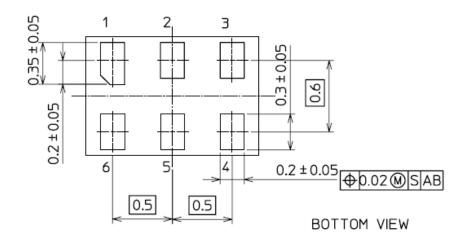


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Package Dimensions

Unit: mm





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Weight: 2.0 mg (typ.)

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